

6550WE, 6550WD**Typical operation, Class A amplifier**

	triode scheme	tetrode scheme
Filament voltage	6.3 v	6.3 v
Plate voltage	250 v	400 v
Screen grid voltage	250 v	225 v
Grid1 bias voltage	- 14 v	- 16.5 v
Amplitude of grid1 voltage	14 v	16.5 v
Constant component of plate quiescent current	140 ma	87 ma
Constant component of grid2 quiescent current	12 ma	4 ma
Load resistance	1.5 kOhm	3 kOhm
Total non-linear distortions	7%	14%
Output power at maximum signal	12.5 w	20 w

Limiting values

	triode scheme		tetrode scheme	
	min	max	min	max
Filament voltage	5.7 v	6.9 v	5.7 v	6.9 v
Plate voltage, DC		440 v		800 v
Screen voltage, DC		-		440 v
Grid1 positive voltage, DC		0 v		0 v
Grid 1 negative voltage		300 v		300 v
Grid 2 negative voltage		440 v		800 v
Plate dissipation		44 w		42 w
Screen dissipation		6.6 w		6.6 w
Cathode current		192.5		230
Constant component of plate current		190 ma		190 ma
Cathode to filament voltage				
Filament voltage is positive to cathode, constant component		100 v		100 v
Filament voltage is positive to cathode (DC, amplitude)		200 v		200 v
Filament voltage is negative to cathode (DC, amplitude)		300 v		300 v
Resistance in grid1 circuit				
at fixed (clamp) bias		0.051 MOh		0.051 MOh
at automatic bias		0.24 MOh		0.24 MOh
Envelope temperature at hottest point		250° C		250° C

